



(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	10/816,103
Filing Date	March 31, 2004
First Named Inventor	Edward T. Grochowski
Art Unit	2183
Examiner Name	Unknown
Attorney Docket Number	42P18305

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Sheet	X 2	of	2	Attorney Docket Number	42P18305

NON PATENT LITERATURE DOCUMENTS			
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RF		TULLSEN et al - January 1999 - www.cs.washington.edu/research/smt/papers/hpca.ps> retrieved on 7/25/2005 - Supporting fine-grained synchronization on a simultaneous multithreading processor - SYMPOSIUM ON HIGH PERFORMANCE COMPUTER ARCHITECTURE Online!	

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RF		ALPERT, DON, "Will Microprocessors Become Simpler?", Multithreading May Reverse the Trend Toward Increasing Processor Complexity, Microprocessor Report, The Insider's Guide To Microprocessor Hardware, www.MPRonline.com, November 17, 2003, pp. 1-4.		
RF		GONTMAKHER, ALEX, et al., "Intrathreads: Techniques For Parallelizing Sequential Code", Abstract, gsash@cs.technion.ac.il, pp. 1-9.		
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RF		SUN MICROSYSTEMS, "MAJCTM Architecture Tutorial", Palo Alto, CA 94303, USA, Internet: www.son.com/microelectronics, pp. 31		
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RF		SIGNORELLI, ROBERT D., "MAJC: Innovation In Parallelism", pp. 1-6.		

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RF		SOHI, GURINDAR S., et al., "Speculative Multithreaded Processors", Cover Feature, University of Wisconsin-Madison, pp. 66-72.		
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RF		SUDHARSANAN, SUDHARSANAN, "MAJC-5200: A High Performance Microprocessor For Multimedia Computing", Sun Microsystems, Inc., Palo Alto, CA 94303, USA, Abstract, pp. 8.		
RF		SUN MICROSYSTEMS, Sun MAJC 5200, "A Processor for the 21st Century", pp. 34.		
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RF		TREMBLAY, MARC, "The MAJC Architecture: A Synthesis Of Parallelism And Scalability", Sun Microsystems, 901 San Antonio Rd., MS USUN03-202, Palo Alto, CA 94303, USA, marc. tremblay @sun.com, November - December 2000, pp. 25		

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